

Microcomputer Principle. Compile and Interface Technology Study Guide

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Reviews

It is straightforward in go through easier to recognize. I actually have study and that i am sure that i will going to study yet again again down the road. Once you begin to read the book, it is extremely difficult to leave it before concluding. (Jaclyn Johns DDS)

MICROCOMPUTER PRINCIPLE. COMPILE AND INTERFACE TECHNOLOGY STUDY GUIDE



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paperback. Condition: New. Ship out in 2 business day, And Fast shipping, Free Tracking number will be provided after the shipment.Pages Number: 166 Publisher: Tsinghua University Press Pub. Date :2006-12-1. This book is edited by the same author's computer principles. assembly and interface technology. supporting the use of the book is in line with Microcomputer Principle and Interface Technology curriculum resource materials prepared. Teaching the book of the important and difficult for a more detailed discussion. and through a typical example of the course content difficult to understand in detail the analysis. but also on the selection of exercises made a more detailed to answer. Designed to help readers broaden thinking. problem-solving methods to master in order to deepen understanding and flexible use. This book can be used as tertiary undergraduate and junior college students to learn principles of micro-computers. assembly language programming. computer interface technology and other courses teaching aids. or as a post-graduate staff review the curriculum reference system. Contents: Chapter 1. the basics of micro-computer key 1.3 1.2 1.1 Executive Summary 1.4 Difficulties with detailed examples and exercises 1.5 Detailed analysis of Chapter 2. assembly language and assembler 2.1 Summary 2.2 Key Difficulties 2.3 2.4 2.5 examples and detailed analysis Detailed exercises and program design in Chapter 3 of the basic technical 3.1 3.3 Executive Summary 3.2 Key Difficulties 3.4 3.5 Exercise examples and detailed analysis Detailed Chapter 4 8086/8088 with bus priority and timing 4.1 Summary 4.2 5 4.3 Difficulties 5.1 Chapter Summary 5.2 Key semiconductor memory Difficulties 5.3 5.4 5.5 Exercise examples and detailed analysis Detailed Chapter 6. the input and output. and interface technology focus 6.1 Summary 6.2 6.4 6.3 Difficulties with detailed examples and exercises 6.5 Detailed analysis of Chapter 7 7.1 Summary 7.2 interrupt key technical difficulty of analysis 7.3 7.4 7.5...

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