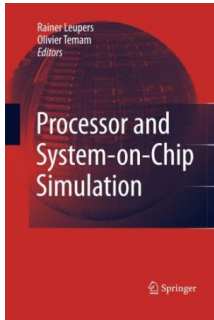Get Book

PROCESSOR AND SYSTEM-ON-CHIP SIMULATION

Condition: New. Publisher/Verlag: Springer, Berlin | This book reviews innovative technologies for simulation of computer architecture, presenting and discussing the principle technologies and current state of high-level hardware architecture simulation, both at the processor and the system-on-chip level. | Simulation of computer architectures has made rapid progress recently. The primary application areas are hardware/software performance estimation and optimization as well as functional and timing verification. Recent, innovative technologies such as retargetable simulator generation, dynamic binary translation, or sampling simulation have...

Download PDF Processor and System-on-Chip Simulation

- Authored by Leupers, Rainer / Temam, Olivier
- Released at -

DOWNLOAD PDF

Filesize: 9.13 MB

Reviews

This is basically the finest pdf i have got study right up until now. I could possibly comprehended almost everything out of this published e book. I am just happy to explain how here is the finest pdf i have got go through in my very own daily life and might be he finest publication for actually.

-- *Emilie Pollich*

Excellent eBook and valuable one. We have read and i am certain that i will going to go through once more yet again later on. You will like how the blogger publish this ebook.

-- *Moriah Jenkins*

Related Books

- Programming in D: Tutorial and Reference
- Programming in D
- JA] early childhood parenting :1-4 Genuine Special(Chinese Edition)
- Violet Rose and the Surprise Party
- Index to the Classified Subject Catalogue of the Buffalo Library; The Whole System Being Adopted from the Classification and Subject Index of Mr. Melvil Dewey, with Some Modifications .