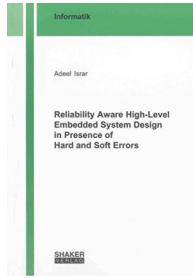


## Reliability Aware High-Level Embedded System Design in presence of Hard and Soft Errors



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(Prof. Hardy Gorczany)

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