

Analysis and Estimation of Jitter Sub-Components

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Condition: New. Publisher/Verlag: LAP Lambert Academic Publishing | Classification and Segregation of Jitter Components | Maintaining quality of signal transmission is a major challenge with the increasing speed of data transmission in Nano-scale VLSI technology. The decreasing voltage margins at the same time, makes it even stringent to maintain Power Integrity (PI) and Signal Integrity (SI) in high speed systems. Jitter is an important phenomenon of signal integrity. This Book contains fundamentals of signal integrity, jitter definition and their classification, summary of previous research work on jitter, a detailed explanation and implementation of different types of jitter estimation techniques using MATLAB. The strength and limitations of tail-fit, improved tail fit, and spectrum approach for jitter segregation are discussed. Further, mathematical models of different jitter sources are implemented. At the end, a new technique for ISI estimation from total jitter using clock pattern is introduced. Authors: Vijender K. Sharma, Rajkumar Nagpal, Dr. Jai Narayan Tripathi, Dr. Sujay Deb, Rakesh Malik. | Format: Paperback | Language/Sprache: english | 96 pp.



Reviews

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