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By ZHU GONG SHENG / ZHU GONG SHENG HU DONG QIN

paperback. Condition: New. Pages Number: 351 Publisher: China Electric Power Press Pub. Date: 2009-07. FPGACPLD System Design and Applications from the start with digital electronic technology. the system to explain the combination of logic and timing logic circuit of the basic knowledge and use of common logic and design. On this basis. to Altera s programmable logic devices. MAX + plus development tools as a platform to explain the application of programmable logic device design method. the final instance through the.

Reviews

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