Find Doc

TURBO CODING



Condition: New. Publisher/Verlag: LAP Lambert Academic Publishing | Hardware Acceleration of an EGPRS-2 Turbo Decoder on an FPGA | This report presents a hardware implementation of an EGPRS-2 turbo decoder based on the soft-output Viterbi algorithm (SOVA). Here techniques for optimizing the implementation has been used to establish a Finite State Machine with Datapath (FSMD) design. EGPRS-2 is the second evolution of GPRS, a standard for wireless transmission of data over the most widespread mobile communication network in the world,...

Download PDF Turbo Coding

- Authored by Kjeldsen, Jesper
- Released at -



Filesize: 9.39 MB

Reviews

This publication is definitely not straightforward to begin on looking at but quite fun to see. It really is loaded with wisdom and knowledge You will not really feel monotony at anytime of your own time (that's what catalogs are for relating to should you check with me).

-- Twila Gutkowski

Most of these ebook is the ideal book offered. It is rally interesting through reading through time. Your way of life span will be enhance the instant you complete reading this ebook.

-- Antonina Friesen

This publication will never be easy to begin on looking at but really fun to read. It is amongst the most incredible publication we have read. I am just pleased to tell you that this is basically the greatest pdf we have study in my very own lifestyle and could be he best book for actually.

-- Modesta Runolfsdottir