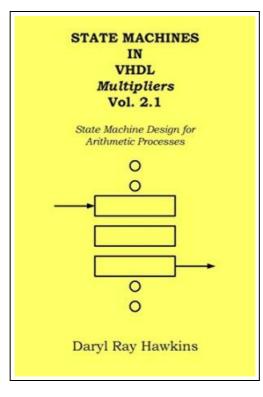
## State Machines in VHDL Multipliers Vol. 2: State Machine Design for Arithmetic Processes



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An exceptional publication as well as the font employed was exciting to see. it was actually writtern extremely flawlessly and helpful. Once you begin to read the book, it is extremely difficult to leave it before concluding. (Dominic Collins)

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